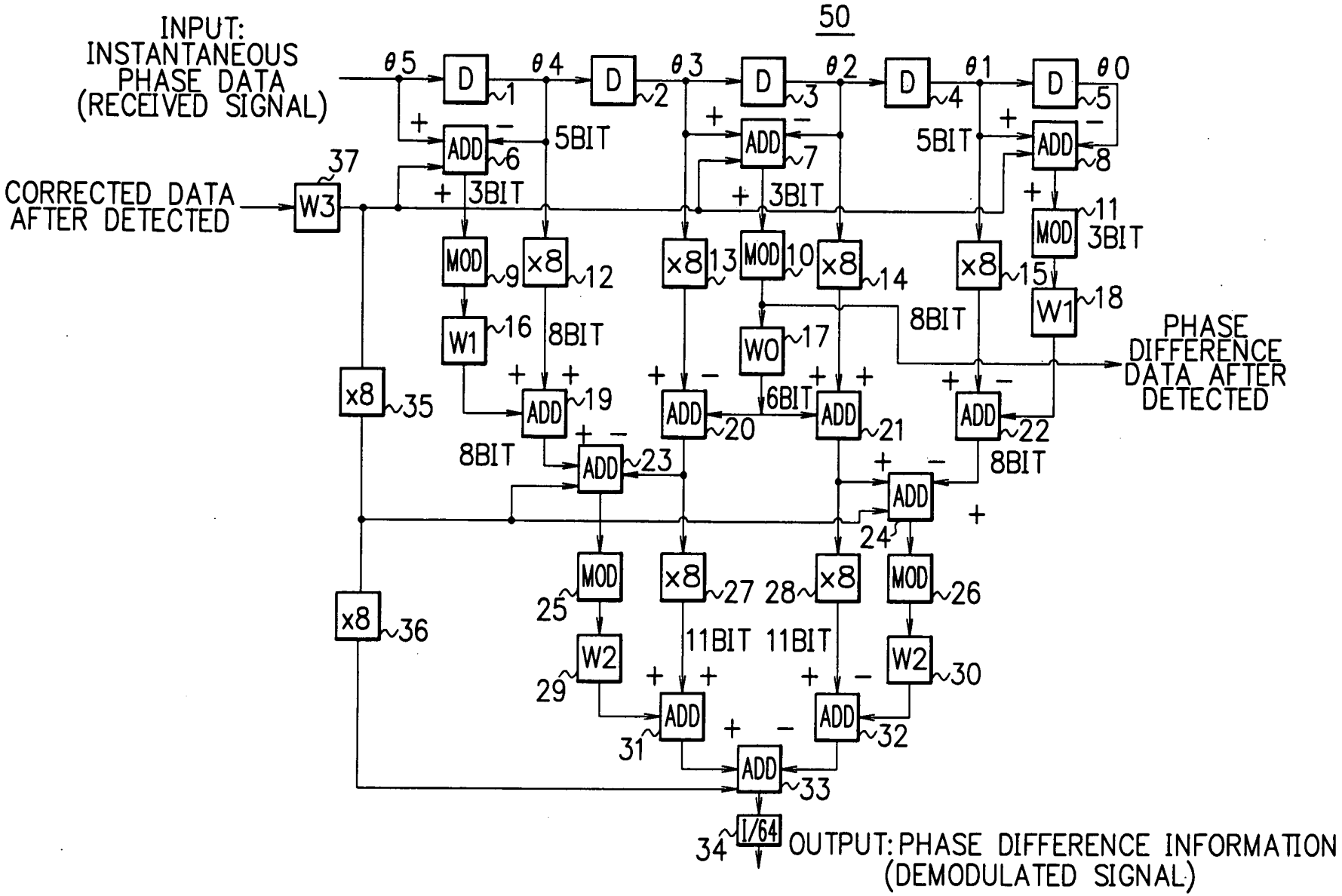
FIG. 1


50
INPUT:
INSTANTANEOUS
PHASE DATA
(RECEIVED SIGNAL)
θ5
θ4
θ3
θ2
θ1
θ0
D
1
2
3
4
5
ADD
6
7
8
5BIT
3BIT
37
W3
CORRECTED DATA
AFTER DETECTED
MOD
9
10
11
3BIT
x8
12
13
14
15
W1
16
18
8BIT
WO
17
PHASE
DIFFERENCE
DATA AFTER
DETECTED
6BIT
19
20
21
22
23
24
35
36
25
26
27
28
11BIT
W2
29
30
31
32
33
I/64
34
OUTPUT: PHASE DIFFERENCE INFORMATION
(DEMODULATED SIGNAL)